

International Conference on Application-Specific Systems, Architectures and Processors 1996: Proceedings : August 19-21, 1996 Chicago, Illinois



These papers address state-of-the-art challenges on topics such as parallel fractal compression, finite element analysis, residue arithmetic, cordic architectures, floating point multiply-accumulate and finite field multiplication, discrete wavelet transform, motion estimate, partitioning and mappin

International Conference on Application-Specific Systems, Architectures and Processors 1996: Proceedings : August 19-21, 1996 Chicago, Illinois. August 19 - 21, 1996. Chicago, Illinois The papers in this book comprise the proceedings of the meeting mentioned on the cover and title International Conference on. Application-Specific Systems, Architectures and Processors - 1 1996. International Conference on Application-Specific Systems, Architectures and Processors 1996: Proceedings : August 19-21, 1996 Chicago, Illinois. by n/a. Abstract: The ALPHA language, based on systems of affine recurrence equations over polyhedral Published in: Application Specific Systems, Architectures and Processors, 1996. Proceedings of International Conference on. Article #: . Date of Conference: 19-21 Aug. 1996 Conference Location: Chicago, IL, USA, USA. International Conference on Application-Specific Systems, Architectures and Processors 1996: Proceedings : August 19-21, 1996 Chicago, Illinois by. Binding: Proceedings of International Conference on. Article #: . Date of Conference: 19-21 Aug. 1996 Published in: Application Specific Systems, Architectures and Processors, 1996. ASAP 96. Conference Location: Chicago, IL, USA, USA. Design of efficient finite field arithmetic architectures is very important and of great Published in: Application Specific Systems, Architectures and Processors, 1996. ASAP 96. Proceedings of International Conference on. Article #: . Date of Conference: 19-21 Aug. 1996 Conference Location: Chicago, IL, USA, USA. In this paper, low power architecture which can support the real time motion estimation of Published in: Application Specific Systems, Architectures and Processors, 1996. ASAP 96. Proceedings of International Conference on. Article #: . Date of Conference: 19-21 Aug. 1996 Conference Location: Chicago, IL, USA, USA. Radix-4 vectoring CORDIC algorithm and architectures. Abstract: In this Published in: Application Specific Systems, Architectures and Processors, 1996. ASAP 96. Proceedings of International Conference on. Article #: . Date of Conference: 19-21 Aug. 1996. Date Added to Conference Location: Chicago, IL, USA, USA. Kestrel is a programmable linear systolic array processor designed for sequence analysis. Published in: Application Specific Systems, Architectures and Processors, 1996. Proceedings of International Conference on. Article #: . Date of Conference: 19-21 Aug. 1996 Conference Location: Chicago, IL, USA, USA. International Conference on Application-Specific Systems, Architectures and Processors 1996: Proceedings : August 19-21, 1996 Chicago, Illinois. IEEE. In-system-programmable, SRAM-based Field Programmable Gate Arrays (FPGAs) can be used to Published in: Application Specific Systems, Architectures and Processors, 1996. Proceedings of International Conference on. Article #: . Date of Conference: 19-21 Aug. 1996 Conference Location: Chicago, IL, USA, USA. Buy a cheap copy of International Conference on book by Jose Fortes. and Processors 1996: Proceedings : August 19-21, 1996 Chicago,

Illinois International Conference on Application-Specific Systems, Architectures and Processors 1996: Proceedings : August 19-21, 1996 Chicago, Illinois. Ieee International Conference on Application-Specific Systems, Architectures and Processors 1996: Proceedings : August 19-21, 1996 Chicago, Illinois [Jose Fortes, International Conference on Application-Specific Systems, Architectures and Processors 1996: Proceedings : August 19-21, 1996 Chicago, Illinois Jose Fortes, Abstract: A flexible block matching motion estimation architecture is described. The block Published in: Application Specific Systems, Architectures and Processors, 1996. ASAP 96. Proceedings of International Conference on. Article #: . Date of Conference: 19-21 Aug. 1996 Conference Location: Chicago, IL, USA, USA. Abstract: In this paper, we describe a VLSI system architecture for real-time intelligent decision Published in: Application Specific Systems, Architectures and Processors, 1996. Proceedings of International Conference on. Article #: . Date of Conference: 19-21 Aug. 1996 Conference Location: Chicago, IL, USA, USA. International Conference on Application-Specific Systems, Architectures and Processors 1996: Proceedings : August 19-21, 1996 Chicago, Illinois Hardcover Results 1 - 25 of 41 Application Specific Systems, Architectures and Processors, 1996. 19-21 Aug. 1996 . Chicago, IL (39) Proceedings of International Conference on Application Specific Systems, Architectures and Processors: ASAP 96. Results 1 - 25 of 41 19-21 Aug. 1996 Sci., Northwestern Univ., Evanston, IL, USA (1). Dept. of Comput. Eng., California Univ., Santa Cruz, Chicago, IL (39). Chicago, IL, USA (2) Proceedings of International Conference on Application Specific Systems, Architectures and Processors: ASAP 96. Publication Year: 1996. the strong sense, and we propose two efficient heuristics. Published in: Application Specific Systems, Architectures and Processors, 1996. ASAP 96. Proceedings of International Conference on. Article #: . Date of Conference: 19-21 Aug. 1996 Publisher: IEEE. Conference Location: Chicago, IL, USA, USA. Advertisement Aggregative systems, AUTOMATION AND REMOTE CONTROL, 1997, Vol.58, No.12 Pt2, pp.1958-1965. on Application-Specific Systems, Architectures and Processors, Proceedings of the 1996 International Conference on Application-Specific Systems, Architectures and Processors Aug. 19-21 1996 Chicago, IL, USA. International Conference on Application-Specific Systems, Architectures and Processors, 1996 : proceedings, August 19-21, 1996 Chicago, Illinois.